The



PATENT Customer No. 22,852 Attorney Docket No. 04329.2613

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:		)	
Katsuhiko HIEDA		)	Group Art Unit: 2814
Application No.: 09/916,509		)	Examiner: Le, Thao X.
Filed:	July 30, 2001	)	
For:	FIELD EFFECT TRANSISTOR USING SIDE-WALL GATES TO PREVENT PUNCH-THROUGH (As Amended)	)	
Comm	issioner for Patents		

P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

## **AMENDMENT**

In reply to the Office Action mailed July 7, 2004, with a period for response extending through October 7, 2004, please amend the above-identified application as follows:

Amendments to the Abstract are shown on page 2 of this paper.

Amendments to the Specification are shown on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims beginning on page 4 of this paper.

Remarks begin on page 20 of this paper.

Attachments to this paper include a Replacement Abstract.